

With DEC already having a number of high-end machineries based on the product, the Alpha looks set for success. It seems this chip has provided the company with the best of both worlds.

The Alpha's prospects against MIFs have been strengthened considerably by Microsoft's announcement that Windows NT would be ported to the wacky architecture. DEC is still hedging its bets by remaining an ACE member and shipping MIFs-based products at the low end of its product line, but the company has the trump card and can use it practically to knock MIFs chip out of its range at any time, promoting Alpha as the chip which runs Windows NT better.

Along with the Alpha, Intel's launch of the Pentium Processor played a large part in ACE's demise. Intel had pulled out of the ACE Consortium because MPPs reduced Intel's importance within the ACT community.

As far as influence goes, the Alpha has far outstripped the MPUs processor, now that MPUs has lost much of the support for its ACE consortium brainchild. Few people apart from MPUs, Acer and SGI, are supporting it. Alpha is supported by Cray, which is using it in its next generation of supercomputers. In addition, DEC has a stake in Olivetti, and the Italian company has included the chip in its own product strategy. And DEC is a \$14 billion outfit with the power to put a lot of market

The chip ranges from 133MHz to the 200MHz clock speed range, which can be used in mainstream applications. It has Subscalar architecture, meaning it can process more than one instruction per clock cycle. It also Superpipelines, enabling it to work on several elements of an instruction at once.

The Alpha has the essential advantage of being faster than the MIPS chip. It is a scalable device, so it can be used in the smallest of microprocessors, like palmtops, right up to supercomputers. The chip, while not a semiautomatic product, represents an increase in speed and efficiency which will speed the computer industry along in terms of available power.

The winner is the Alpha chip from DEC, the 64-bit answer to MIPS' RISC processor which now appears to be dead.

bonnet, it should have the power to do what it has to. It will also have a PCMCIA 2.0 compatible slot for add-in products, and software for it will become available on cards when the machine is released. Apple hints at other products based on Newton technology, such as the Newton Draw and Spell for children, the Newton Fax Phone, the Newton Port-a-able Map and the Newton Classroom. At under \$1000, this machine could drag the electronic playpen into the real world.

The handset held unit, which resembles the telephone, will not only be able to recognise your handwritting but will learn its idiosyncrasies the more you use it. The machine will be able to work the way you do, scheduling your appointments on the miniium of information: LUNCH ANNE FRI-DAY. Other automatic tasks such as faxing, and totting up columns of figures when you draw a line under them, mean the tool will be invaluable.

A detailed micrograph of a microchip, likely a central processing unit (CPU). The chip features a complex arrangement of rectangular regions in shades of brown, tan, and grey, representing different types of transistors and interconnects. Numerous thin, dark lines form a dense network of horizontal and vertical connections. On the left side, there are several large, rectangular pads, which are bond pads used for connecting the chip to an external package. The overall structure is highly symmetrical and organized.



**Most Innovative or Influential
Hardware: DEC Alpha**